I. INTRODUCTION

A. A study of various semiconductor devices incorporated in circuits and their applications. Emphasis on circuit construction, measurements, and analysis.

B. This course serves as a required or elective course on various degree plans. Curriculum plans for degrees and certificates, are listed in the current Central Texas College Catalog.

C. The delivery method of this course may be traditional lecture/lab, blended lecture/lab, or online.

D. Prerequisites: CETT 1429 Solid State Devices.

II. LEARNING OUTCOMES

Upon successful completion of this course, Solid State Circuits, the student will be able to:

- Construct, analyze, test, and troubleshoot circuits containing various semiconductor devices. (C9, C18, C19, C20, F1)

III. INSTRUCTIONAL MATERIALS

A. The instructional materials identified for this course are viewable through www.ctcd.edu/books

B. Lecture Classes may also require a USB storage device. Online students may use cloud based storage.

IV. COURSE REQUIREMENTS
A. Attend both lecture and lab or in the case of online delivery, be actively engaged in Blackboard and maintain constant progress.

B. Be prepared to participate in discussion, team projects/assignments and take unannounced assessments relating to the lecture materials.

C. Complete all exams/assessments.

D. Submit all assignments on time.

V. ASSESSMENTS

A. Student content mastery will be evaluated in the following areas:
   • Quizzes (one for each of the units covered)
   • Midterm Exam
   • Final Assessment (you must score at least 70% to pass the course)

B. Scheduled and unscheduled assessments will be given at the discretion of the instructor.

C. Exams/assessments may be composed of both subjective and objective questions plus computer output. Failure to pass the Final Exam will result in a maximum grade of “D” for the entire course.

D. A student must take all exams/assessments. No make-up exams/assessments will be given. Both online and on campus students who know in advance that they will be absent due to school sponsored trips, military duty or orders, or any other valid reason, must arrange to take an early exam/assessment. Unexpected absences due to illness or other extenuating circumstances will require the student to see the instructor about make-up work in lieu of the missed exam/assessment.

E. Students with unexcused absences will be given a zero for each missed assignment.

VI. SEMESTER GRADE COMPUTATIONS

<table>
<thead>
<tr>
<th>Course Requirements</th>
<th>Points</th>
<th>Points</th>
<th>Grade</th>
<th>Quality Points</th>
</tr>
</thead>
<tbody>
<tr>
<td>Assignments &amp; Quizzes</td>
<td>350</td>
<td>900-1000</td>
<td>A-Superior</td>
<td>4</td>
</tr>
<tr>
<td>Labs</td>
<td>350</td>
<td>800-899</td>
<td>B-Above Average</td>
<td>3</td>
</tr>
<tr>
<td>Midterm</td>
<td>100</td>
<td>700-799</td>
<td>C-Average</td>
<td>2</td>
</tr>
<tr>
<td>Final Assessment*</td>
<td>200</td>
<td>600-699</td>
<td>D – Passing but Unsatisfactory</td>
<td>1</td>
</tr>
</tbody>
</table>
VII. NOTES AND ADDITIONAL INSTRUCTIONS FROM THE INSTRUCTOR

A. **Course Withdrawal:** It is the student’s responsibility to officially withdraw from a course if circumstances prevent attendance. Any student who desires to, or must, officially withdraw from a course after the first scheduled class meeting must file a Central Texas College Application for Withdrawal (CTC Form 59). The student must sign the withdrawal form.

CTC Form 59 will be accepted at any time prior to Friday of the 12th week of classes during the 16-week fall and spring semesters. The deadline for sessions of other lengths is:
- 10-week session: Friday of the 8th week
- 8-week session: Friday of the 6th week
- 5-week session: Friday of the 4th week
The equivalent date (75% of the semester) will be used for sessions of other lengths. The specific last day to withdraw is published each semester in the Schedule Bulletin.

For non-GoArmyEd active military students, the effective date of withdrawal is the filing date with the Education Center. For all other students, the effective date of withdrawal is the date that the withdrawal application is received by the Central Texas College representative.

Students who used financial aid, military tuition assistance, VA benefits, or other non-personal funds may be required to repay tuition and fees to the funding agency. For specific repayment requirements, contact the Office of Student Financial Aid or Veterans Services Office before withdrawing. Military tuition assistance students should visit their military Education Center or Navy College Office.

A student who officially withdraws will be awarded the grade of “W” provided the student’s attendance and academic performance are satisfactory at the time of official withdrawal. Students must file a withdrawal application with the College before they may be considered for withdrawal.

A student may not withdraw from a class for which the instructor has previously issued a grade of “F,” “FI,” “FN,” “IP,” or “XN.”

B. **Instructor Initiated Withdrawals:** Faculty are authorized to withdraw students who are not making satisfactory course progress to include failure to meet College attendance requirements as outlined in the section of the Catalog entitled “Satisfactory Progress Standards.” The instructor will assign the appropriate grade on CTC Form 59 for submission to the registrar.
Students enrolled in distance learning courses are expected to maintain constant progress throughout the course. Failure to do so may result in the student being administratively withdrawn by the instructor.

Students who have not attended class by the 12th class day of a 16-week course or the 6th class day of an 8-week term may be administratively withdrawn by the instructor with a grade of "W." Students may be administratively withdrawn from any class when their absences reach a total equal to 12.5% of the class hours for the course; and in the opinion of the instructor, the student cannot satisfactorily complete the course. An example: Students attending a 48-hour class during an 8-week period normally meet 180 minutes each session for 16 sessions. Those students accumulating two (2) unexcused absences are subject to Administrative Withdrawal since the total unexcused absences equal 12.5% of class hours for the course. Those students attending a 48 hour class during a 16-week period normally meet 90 minutes each session for 32 sessions. Those students accumulating four (4) unexcused absences are subject to Administrative Withdrawal since the total unexcused absences equals 12.5% of class hours for the course. In a distance learning course the last date of attendance is the last activity by the student in the course.

C. **Administrative Withdrawal:** A student may be administratively withdrawn by a designated member of the administrative staff of the College when the student has been placed on Academic Suspension or Disciplinary Suspension; the student has an outstanding financial obligation owed to the college; or the student registered for a course without the required prerequisite or departmental permission.

The College is under no obligation to refund tuition and fees, or other costs associated with an administrative or instructor initiated withdrawal.

D. **Incomplete Grade:** The College catalog states, “An incomplete grade may be given in those cases where the student has completed the majority of the coursework but, because of personal illness, death in the immediate family, or military orders, the student is unable to complete the requirements for a course...” Prior approval from the instructor is required before the grade of “IP” for Incomplete is recorded.

E. **Cell Phones and Pagers:** Students will silence cell phones and mobile devices while in the classroom or lab.

F. **Americans with Disabilities Act (ADA):** Disability Support Services provide services to students who have appropriate documentation of a disability. Students requiring accommodations for class are responsible for contacting the Office of Disability Support Services (DSS) located on the central campus. This service is available to all students, regardless of location. Review the website at [www.ctcd.edu/disability-support](http://www.ctcd.edu/disability-support) for further information. Reasonable
accommodations will be given in accordance with the federal and state laws through the DSS office.

G. Instructor Discretion: The instructor reserves the right of final decision in course requirements and may make changes to the course outline and/or assignments as needed.

H. Civility: Individuals are expected to be aware of what a constructive educational experience is and be respectful of those participating in a learning environment. Failure to do so can result in disciplinary action up to and including expulsion.

I. Degree Progression: Students who receive a grade of “D” are advised not to enroll in the next course for which this course was a prerequisite.

J. Failing Grade: The grade of “F” or “FN” will be given for academic failure, non-attendance or scholastic dishonesty.

K. Scholastic Honesty: All students are expected to maintain the highest standards of scholastic honesty in the preparation of all course work and during examinations. The college policy on scholastic honesty, including definitions on plagiarism, collusion, and cheating can be found at the following URL: [http://online.ctcd.edu/plagiarism.cfm](http://online.ctcd.edu/plagiarism.cfm)
VIII. COURSE OUTLINE

A. Lesson One: Field Effect Transistors

1. **Learning Outcomes**: Upon successful completion of this lesson the student will be able to:
   a. Explain the operation of JFETS
   b. Define, discuss, and apply JFET parameters
   c. Discuss and analyze JFET circuits
   d. Explain the operation of MOSFETS
   e. Define, discuss, and apply MOSFET parameters
   f. Discuss and analyze MOSFET bias circuits.
   g. Troubleshoot FET circuits

2. **Learning Activities**
   a. Research and discuss the topics of the Lesson in class and in an online collaborative discussion forum (C7, C8, C9, C15, C18, F1, F9, F10, F13)
   b. Perform skills and functions in the section (C1, C5, C7, C8, C9, C16, C18, F1, F9, F10, F13)

3. **Lesson Outline**:
   a. The JFET
   b. JFET characteristics and parameters
   c. Biasing
   d. The MOSFET
   e. MOSFET characteristics and parameters
   f. Biasing
   g. Troubleshooting.

B. Lesson Two: FET Amplifiers

1. **Learning Outcomes**: Upon successful completion of this lesson the student will be able to:
   a. Describe the amplification properties of a FET
   b. Explain and analyze the operation of common-source FET amplifiers.
   c. Explain and analyze the operation of common-drain FET amplifiers.
   d. Explain and analyze the operation of common-gate FET amplifiers.
   e. Troubleshoot FET amplifiers.

2. **Learning Activities**:
   a. Research and discuss the topics of the Lesson in class and in an online collaborative discussion forum (C7, C8, C9, C15, C18, F1, F9, F10, F13)
   b. Perform skills and functions in the section (C1, C5, C7, C8, C9, C16, C18, F1, F9, F10, F13)
3. **Lesson Outline:**
   a. FET Amplification
   b. Common-source amplifiers
   c. Common-gate amplifiers
   d. Amplifier Frequency response

C. **Lesson Three: Operational Amplifiers**

1. **Learning Outcomes:** Upon successful completion of this lesson the student will be able to:
   a. Describe the basic op-amp and its characteristics.
   b. Discuss op amp models and parameters.
   c. Explain negative feedback
   d. Analyze the non-inverting, voltage follower, and inverting op-amp configurations.
   e. Describe the impedance of the above three configurations.
   f. Discuss op-amp compensation.
   g. Analyze open loop response.
   h. Analyze closed loop response.
   i. Troubleshoot op-amp circuits.

2. **Learning Activities:**
   a. Research and discuss the topics of the Lesson in class and in an online collaborative discussion forum (C7, C8, C9, C15, C18, F1, F9, F10, F13)
   b. Perform skills and functions in the section (C1, C5, C7, C8, C9, C16, C18, F1, F9, F10, F13)

3. **Lesson Outline:**
   a. Introduction to op-amps.
   b. Op-amp input modes and parameters.
   c. Negative feedback
   d. Op-amps with negative feedback.
   e. Effects of negative feedback on op-amp impedance.
   f. Bias current and offset voltage compensation.
   g. Open loop response.
   h. Closed loop response.
   i. Troubleshooting.

D. **Lesson Four: Basic Op-Amp Applications**

1. **Learning Outcomes:** Upon successful completion of this lesson the student will be able to:
   a. Analyze the operation of several comparator circuits.
b. Analyze summing amplifiers.
c. Analyze integrators and differentiators.
d. Troubleshoot op-amp circuits.

2. **Learning Activities:**
   a. Research and discuss the topics of the Lesson in class and in an online collaborative discussion forum (C7, C8, C9, C15, C18, F1, F9, F10, F13)
   b. Perform skills and functions in the section (C1, C5, C7, C8, C9, C16, C18, F1, F9, F10, F13)
   c. Homework – All Problems Chapter 4 (F4)
   d. Lab experiment 30-31. (Week 12 and 13) (C9, C18, C19, C20)

3. **Lesson Outline:**
   a. Comparators
   b. Summing Amplifiers
   c. Integrators and differentiators
   d. Troubleshooting.

E. **Lesson Five: Thyristors and other devices**

1. **Learning Outcomes:** Upon successful completion of this lesson the student will be able to:
   a. Describe the basic structure and operation of a 4-layer diode.
   b. Describe the basic structure and operation of the SCR.
   c. Discuss SCR applications.
   d. Describe the basic operation and structure of DIACS and TRIACS
   e. Describe the operation of an SCS.
   f. Describe the structure of the PUT.
   g. Describe the phototransistor.
   h. Describe the LASCR and its operation.
   i. Discuss the various types of opto-isolators.

2. **Learning Activities:**
   a. Classroom Lectures/Discussion (F5, F6)
   b. Reading Assignment (F1)
      1). Read Chapter 11

3. **Lesson Outline:**
   a. The basic 4-layer device.
   b. The SCR
   c. SCR applications
   d. Diacs and Triacs
   e. The SCS
   f. The UJT
g. The PUT
h. The phototransistor
i. The LASCR
j. Optocouplers